1588*Tiny* : IEEE1588 V2 Slave Only *hard* IP Core
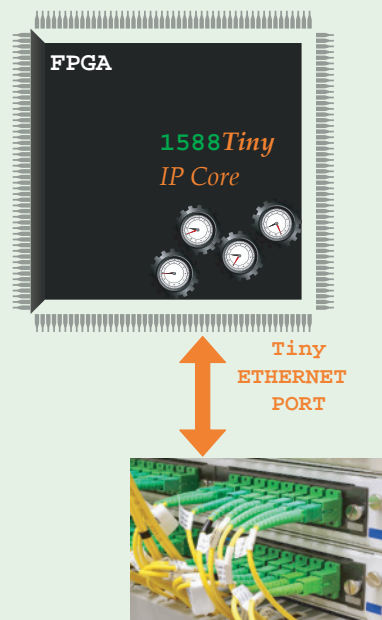## Accurate synchronization with '1588 in a nutshell'

General description

1588*Tiny* is the simplest and smallest solution to add IEEE1588-2008 V2 functionality to any equipment. **1588***Tiny* main features:

- It is a single IP core that comprises IEEE1588-2008 V2 Slave Only Functionalities and a basic Ethernet Port to process PTP frames
- It offers high accuracy clock synchronized with a IEEE1588 Master connected to Ethernet net with the minimum FPGA resources
- It is a full *hardware* solution. **Software PTP stack is not required, nor soft embedded Microprocessor**
- Excellent trade-off between clock accuracy and resources. For extra clock accuracy, see **PreciseTime***Basic* IP core
- Standard versions are available for low-cost Xilinx FPGAs

FPGA

1588*Tiny*
IP Core

Tiny ETHERNET PORT

Applications

By its implementation modularity **1588***Tiny* may be used in a wide range of applications. It can be implemented seamlessly in low-cost or high-end FPGA families. Among other sectors where **1588***Tiny* can be directly used, highlight:

- Equipments for Energy Market
- Industrial Ethernet communications
- Wired Networks synchronization
- Home Automation
- Military and Aerospace

Basic Package

1588*Tiny* basic package includes the following items:

- IP core netlist ready for seamless integration in ISE design flow
- Reference design for AVNET Spartan-6 FPGA LX9 Microboard
- Available profiles: L2 Default, Power and IEC 61850
- Training seminar

SoC*e*